

**EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L28	0	(clock distribution circuit AND first clock circuit AND first clock signal AND second clock circuit AND second clock signal AND error signal AND input AND clock input). clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/03/28 16:08

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